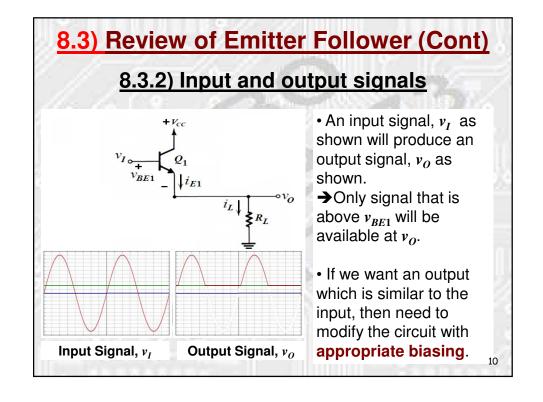
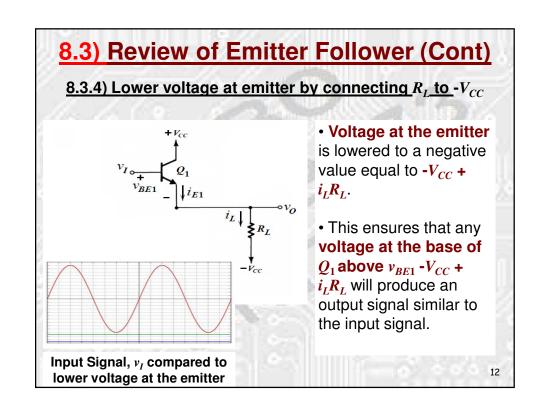
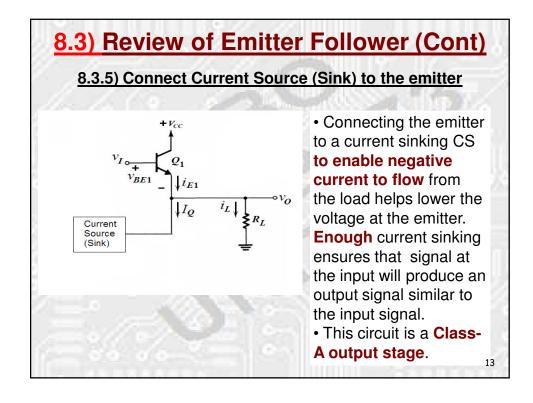
8.3) Review of Emitter Follower 8.3.1) Circuit diagram • Emitter follower circuit is a Common Collector amplifier. • The circuit has low output resistance.

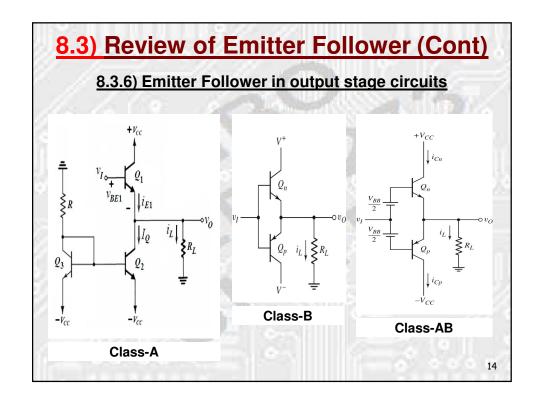


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8.3) Review of Emitter Follower (Cont) 8.3.3) Bias with positive voltage at base By voltage biasing at the base, the input signal v_I is added with the bias voltage. This ensures that voltage at the base of Q_1 is always above v_{BE1} and this in turn will produce an output signal similar to the input signal.







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