

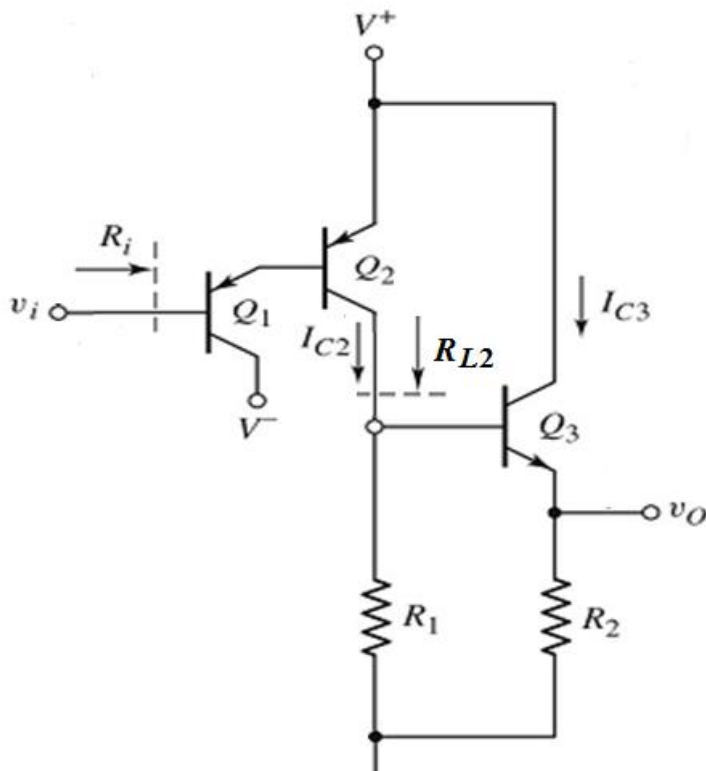
EEEE2014/EEEE273 - Quiz 3
 SEMESTER 1, ACADEMIC YEAR 2019/2020
 Date: 24 July 2019 Time: 15 minutes

Question:

Study the gain stage and simple output stage circuit shown in **Figure 1** carefully. The transistor parameters are: $\beta = 100$ and $V_A = 120$ V.

Let $R_1 = 50$ k Ω , $R_2 = 5$ k Ω , $I_{C2} = 0.54$ mA, and $I_{C3} = 1.2$ mA. Determine the input resistance (R_i) of the gain stage and the equivalent load resistance of the gain stage connected to the collector of Q_2 , i.e. R_{L2} shown in the **Figure 1**. [10 marks]

Write your answer using pen, with proper Units for all the parameters.



$$i_C = I_S e^{v_{BE}/V_T}; \text{npn}$$

$$i_C = I_S e^{v_{EB}/V_T}; \text{pnp}$$

$$i_C = \alpha i_E = \beta i_B$$

$$i_E = i_B + i_C$$

$$\alpha = \frac{\beta}{\beta + 1}$$

; Small signal

$$\beta = g_m r_\pi$$

$$r_\pi = \frac{\beta V_T}{I_{CQ}}$$

$$g_m = \frac{I_{CQ}}{V_T}$$

$$r_o = \frac{V_A}{I_{CQ}}$$

$$V_T = 26 \text{ mV}$$

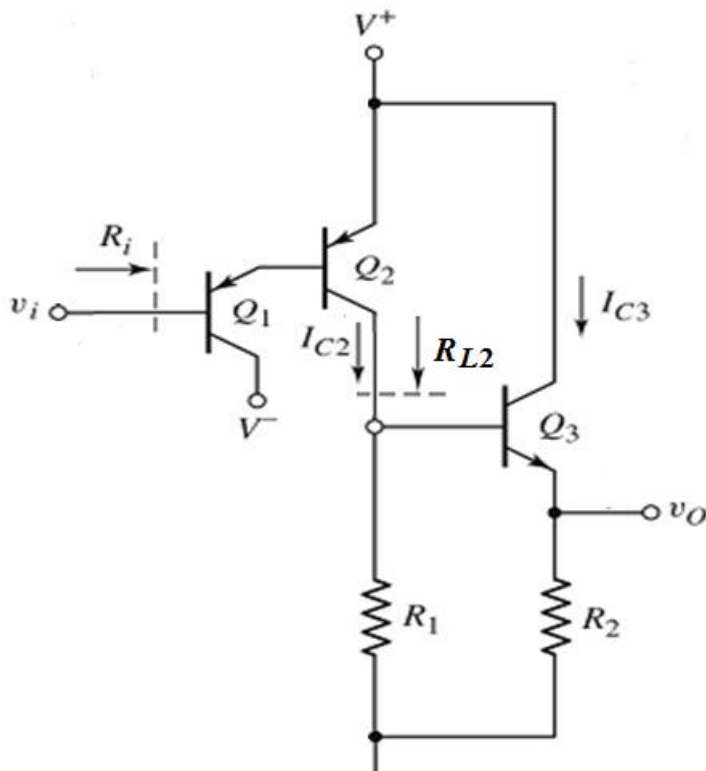
R_i	$= r_{\pi 1} + (1 + \beta) r_{\pi 2}$	[2]
$r_{\pi 2}$	$= (\beta V_T) / I_{C2} = (100 \times 0.026) / 0.54\text{m} = 4.81 \text{ k}\Omega$	[1]
I_{C1}	$= I_{C2} / (1 + \beta)$	[1]
$r_{\pi 1}$	$= (\beta V_T) / I_{C1} = (\beta(1 + \beta) V_T) / I_{C2}$ $= (100 \times 101 \times 0.026) / 0.54\text{m} = 486.3 \text{ k}\Omega$	[1]
R_i	$= r_{\pi 1} + (1 + \beta) r_{\pi 2}$ $= 486.3\text{k} + (101)(4.81\text{k}) = 972.4 \text{ k}\Omega$	[1]
R_{L2}	$= R_1 // [r_{\pi 3} + (1 + \beta) R_2]$	[2]
$r_{\pi 3}$	$= (\beta V_T) / I_{C3} = (100 \times 0.026) / 1.2\text{m} = 2.17 \text{ k}\Omega$	[1]
R_{L2}	$= (50\text{k}) // [2.17\text{k} + (1 + 100)(5\text{k})] = 45.51 \text{ k}\Omega$	[1]

Question:

Study the gain stage and simple output stage circuit shown in **Figure 1** carefully. The transistor parameters are: $\beta = 100$ and $V_A = 120$ V.

Let $R_1 = 40$ k Ω , $R_2 = 8$ k Ω , $I_{C2} = 0.42$ mA, and $I_{C3} = 1.3$ mA. Determine the input resistance (R_i) of the gain stage and the equivalent load resistance of the gain stage connected to the collector of Q_2 , i.e. R_{L2} shown in the **Figure 1**. [10 marks]

Write your answer using pen, with proper Units for all the parameters.



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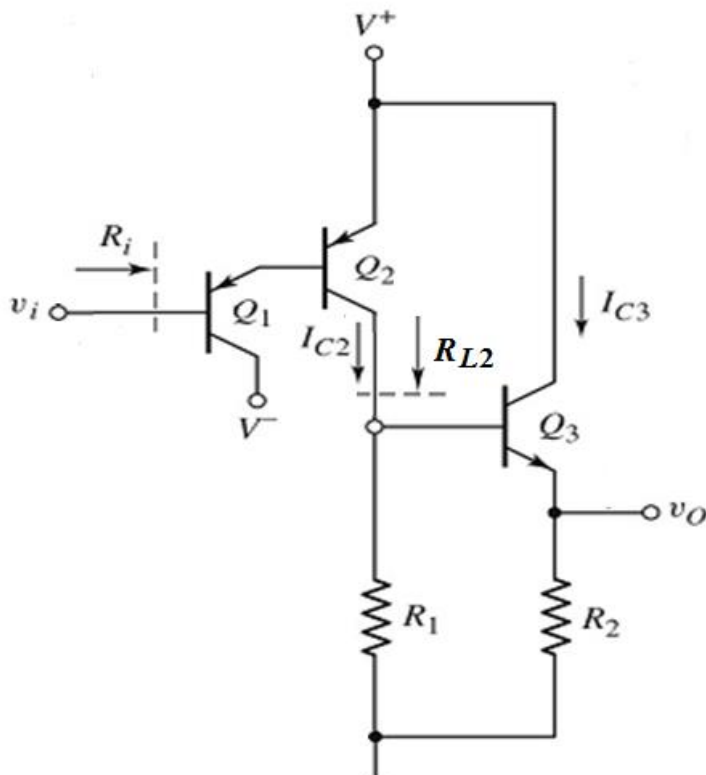
R_i	$= r_{\pi 1} + (1 + \beta) r_{\pi 2}$	[2]
$r_{\pi 2}$	$= (\beta V_T) / I_{C2} = (100 \times 0.026) / 0.42\text{m} = 6.19 \text{ k}\Omega$	[1]
I_{C1}	$= I_{C2} / (1 + \beta)$	[1]
$r_{\pi 1}$	$= (\beta V_T) / I_{C1} = (\beta(1 + \beta) V_T) / I_{C2}$ $= (100 \times 101 \times 0.026) / 0.42\text{m} = 625.24 \text{ k}\Omega$	[1]
R_i	$= r_{\pi 1} + (1 + \beta) r_{\pi 2}$ $= 625.24\text{k} + (101)(6.19\text{k}) = 1250.43 \text{ k}\Omega$	[1]
R_{L2}	$= R_1 // [r_{\pi 3} + (1 + \beta) R_2]$	[2]
$r_{\pi 3}$	$= (\beta V_T) / I_{C3} = (100 \times 0.026) / 1.3\text{m} = 2 \text{ k}\Omega$	[1]
R_{L2}	$= (40\text{k}) // [2\text{k} + (1 + 100)(8\text{k})] = 38.12 \text{ k}\Omega$	[1]

Question:

Study the gain stage and simple output stage circuit shown in **Figure 1** carefully. The transistor parameters are: $\beta = 100$ and $V_A = 120$ V.

Let $R_1 = 55$ k Ω , $R_2 = 8$ k Ω , $I_{C2} = 0.36$ mA, and $I_{C3} = 1.4$ mA. Determine the input resistance (R_i) of the gain stage and the equivalent load resistance of the gain stage connected to the collector of Q_2 , i.e. R_{L2} shown in the **Figure 1**. [10 marks]

Write your answer using pen, with proper Units for all the parameters.



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; Small signal

$$\beta = g_m r_\pi$$

$$r_\pi = \frac{\beta V_T}{I_{CQ}}$$

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$$V_T = 26 \text{ mV}$$

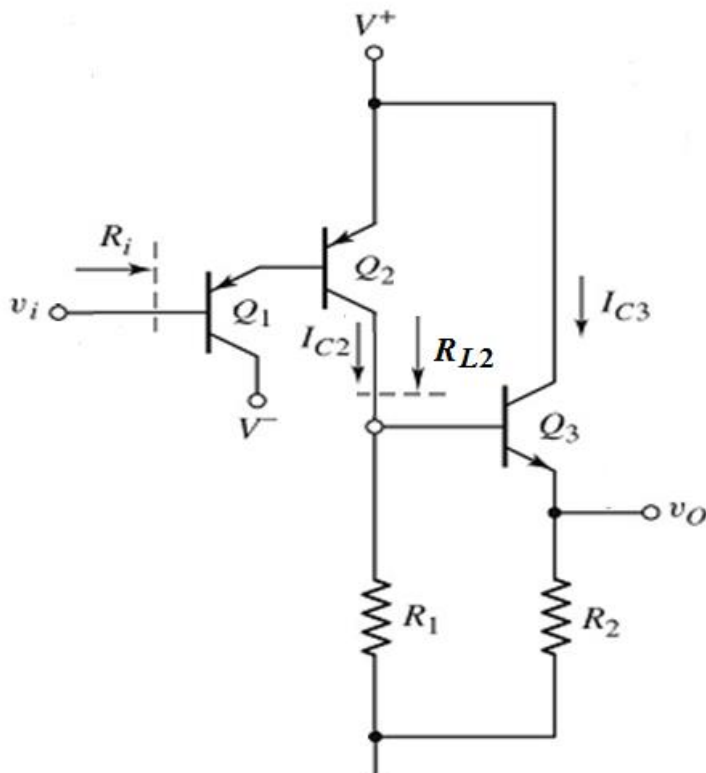
R_i	$= r_{\pi 1} + (1 + \beta) r_{\pi 2}$	[2]
$r_{\pi 2}$	$= (\beta V_T) / I_{C2} = (100 \times 0.026) / 0.36\text{m} = 7.222 \text{ k}\Omega$	[1]
I_{C1}	$= I_{C2} / (1 + \beta)$	[1]
$r_{\pi 1}$	$= (\beta V_T) / I_{C1} = (\beta(1 + \beta) V_T) / I_{C2}$ $= (100 \times 101 \times 0.026) / 0.36\text{m} = 729.444 \text{ k}\Omega$	[1]
R_i	$= r_{\pi 1} + (1 + \beta) r_{\pi 2}$ $= 729.444\text{k} + (101)(7.222\text{k}) = 1458.88 \text{ k}\Omega$	[1]
R_{L2}	$= R_1 \parallel [r_{\pi 3} + (1 + \beta) R_2]$	[2]
$r_{\pi 3}$	$= (\beta V_T) / I_{C3} = (100 \times 0.026) / 1.4\text{m} = 1.86 \text{ k}\Omega$	[1]
R_{L2}	$= (55\text{k}) \parallel [1.86\text{k} + (1 + 100)(8\text{k})] = 51.5 \text{ k}\Omega$	[1]

Question:

Study the gain stage and simple output stage circuit shown in **Figure 1** carefully. The transistor parameters are: $\beta = 100$ and $V_A = 120$ V.

Let $R_1 = 45$ k Ω , $R_2 = 10$ k Ω , $I_{C2} = 0.62$ mA, and $I_{C3} = 1.5$ mA. Determine the input resistance (R_i) of the gain stage and the equivalent load resistance of the gain stage connected to the collector of Q_2 , i.e. R_{L2} shown in the **Figure 1**. [10 marks]

Write your answer using pen, with proper Units for all the parameters.



$$i_C = I_S e^{v_{BE}/V_T}; \text{npn}$$

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$$g_m = \frac{I_{CQ}}{V_T}$$

$$r_o = \frac{V_A}{I_{CQ}}$$

$$V_T = 26 \text{ mV}$$

$$R_i = r_{\pi 1} + (1 + \beta) r_{\pi 2} \quad [2]$$

$$r_{\pi 2} = (\beta V_T) / I_{C2} = (100 \times 0.026) / 0.62\text{m} = 4.193 \text{ k}\Omega \quad [1]$$

$$I_{C1} = I_{C2} / (1 + \beta) \quad [1]$$

$$r_{\pi 1} = (\beta V_T) / I_{C1} = (\beta(1 + \beta) V_T) / I_{C2} = (100 \times 101 \times 0.026) / 0.62\text{m} = 423.548 \text{ k}\Omega \quad [1]$$

$$R_i = r_{\pi 1} + (1 + \beta) r_{\pi 2} = 423.548\text{k} + (101)(4.193\text{k}) = 847.04 \text{ k}\Omega \quad [1]$$

$$R_{L2} = R_1 // [r_{\pi 3} + (1 + \beta) R_2] \quad [2]$$

$$r_{\pi 3} = (\beta V_T) / I_{C3} = (100 \times 0.026) / 1.5\text{m} = 1.73 \text{ k}\Omega \quad [1]$$

$$R_{L2} = (45\text{k}) // [1.73\text{k} + (1 + 100)(10\text{k})] = 43.08 \text{ k}\Omega \quad [1]$$