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 Table Number:



College of Engineering
 Department of Electronics and Communication Engineering

Test 2

SEMESTER 2, ACADEMIC YEAR 2017/2018

Subject Code : **EEEEB273**
 Course Title : **Electronics Analysis & Design II**
 Date : **30 December 2017**
 Time Allowed : **2 hours**

Instructions to the candidates:

1. Write your Name and Student ID Number. Indicate your Section Number and Lecturer's Name. Write also your Table Number.
2. **Write all your answers using pen. DO NOT USE PENCIL** except for the diagram.
3. **ANSWER ALL QUESTIONS.** Show clearly all your calculations. Every value **must** be written with its correct Unit.
4. **WRITE YOUR ANSWER ON THIS QUESTION PAPER.**

NOTE: DO NOT OPEN THE QUESTION PAPER UNTIL INSTRUCTED TO DO SO.

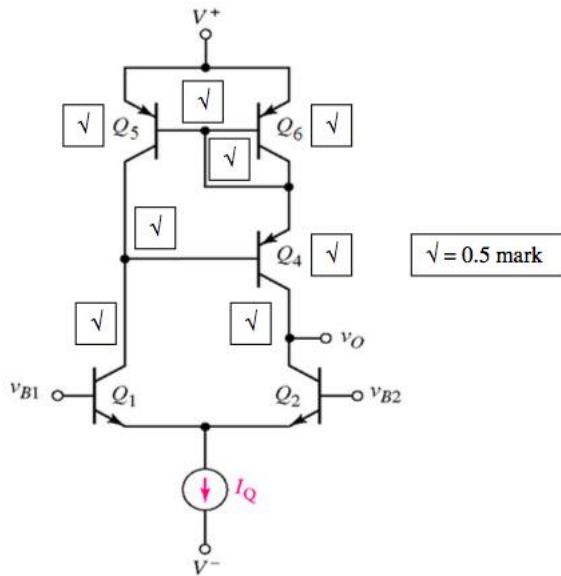
☺ **GOOD LUCK!** ☺

Question Number	Q1a	Q1bc	Q2ad	Q2e	Q3	Q4a	Q4b	Total
Marks								

QUESTION 1 [20 marks]

Answers for Question 1

(a) New diff-amp with BJT Wilson current source as active load drawing



[4]

(b) $A_d = g_{m2} (r_{o2} \parallel R_{OAL})$

[2]

$g_{m2} = I_{CQ}/V_T = (0.23\text{m}/2)/0.026 = 4.423 \text{ mA/V}$

[1, 1, 1]

$r_{o2} = V_{A2}/I_{CQ} = 100/(0.23\text{m}/2) = 0.8696 \text{ M}\Omega$

[1, 1, 1]

$R_{OAL} = (\beta/2) r_{o4} = (\beta/2) V_{A4}/I_{CQ} = (120/2)*120/(0.23\text{m}/2)$

$= 62.609 \text{ M}\Omega$

[2]

So, $A_d = (4.423\text{m})(0.8696\text{M} \parallel 62.609\text{M})$

$= (4.423\text{m})(0.8577\text{M}) = 3.793$

[2]

(c) With the 3-transistor current source active load:

$A_d = g_{m2} (r_{o2} \parallel R_{OAL}) = g_{m2} (r_{o2} \parallel r_{o4})$

$r_{o4} = V_{A4}/I_{CQ} = 120/(0.23\text{m}/2) = 1.043 \text{ M}\Omega$

So, $A_d = (4.423\text{m})(0.8696\text{M} \parallel 1.043\text{M}) = 2.097$

Calculation [2]

Thus, the differential-mode gain with a three-transistor current source active load is reduced approximately 45% because R_o of the 3-transistor current source active load is lower by $\beta/2$ compared to the R_o of the Wilson CS.

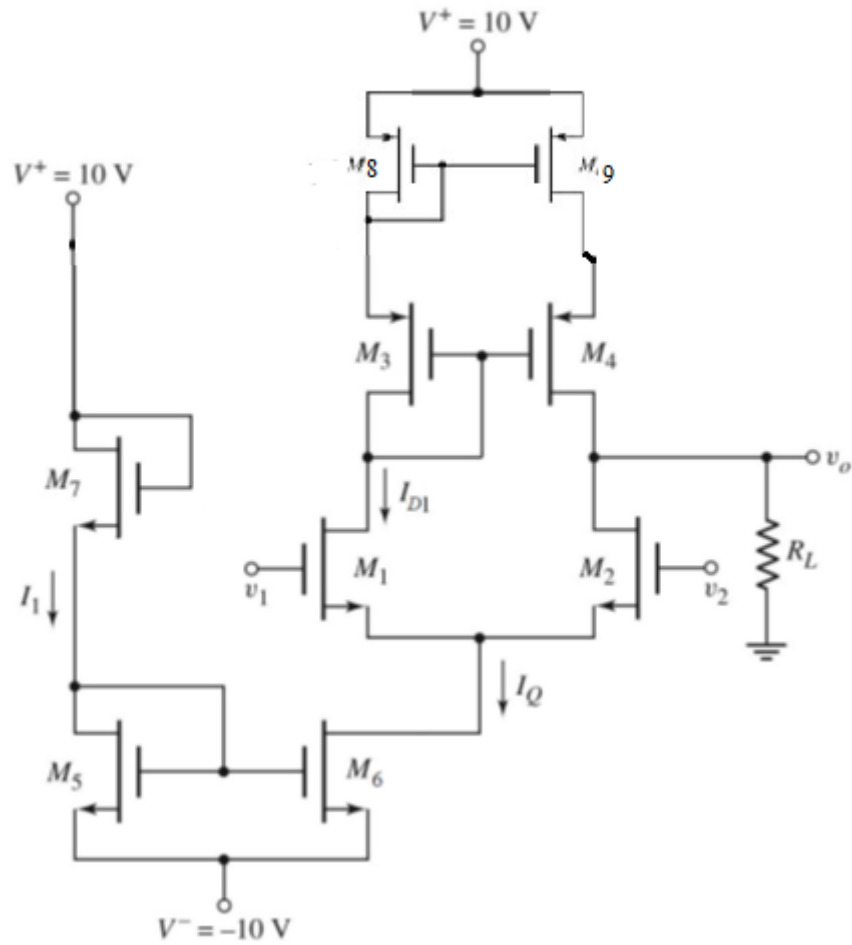
[2]

QUESTION 2 [35 marks]**Answers for Question 2**

- (a) $V_{GS5} = V_{GS7}$ {Identical transistors} [1]
 $V_{GS5} + V_{GS7} = 2 V_{GS5} = V^+ - V^- = 10 - (-10) = 20 \text{ V}$ [0.5]
 $V_{GS5} = 10 \text{ V} = V_{GS7}$ [0.5]
 $I_1 = K_n (V_{GS5} - V_{TN})^2 = K_n (V_{GS7} - V_{TN})^2$ [1]
 $I_1 = (0.2\text{m})(10-2)^2 = 12.8 \text{ mA}$ [1]
 $I_Q = I_1 = 12.8 \text{ mA}$ [1]
 $I_{D1} = \frac{1}{2} I_Q = 6.4 \text{ mA}$ [1]
- (b) $v_o = (g_m)v_d (r_{o2} \parallel r_{o4} \parallel R_L)$ therefore $A_d = (g_m)(r_{o2} \parallel r_{o4} \parallel R_L)$ [2]
 $g_m = 2\sqrt{(K_n I_{D1})} = 2\sqrt{[(0.2\text{m})(6.4\text{m})]} = 2.26 \text{ mA/V}$ [1, 0.5]
 $r_{o2} = 1/(\lambda_n I_{D1}) = 1/(0.02)(6.4\text{m}) = 7.812 \text{ k}\Omega$ [1, 0.5]
 $r_{o4} = 1/(\lambda_p I_{D1}) = 1/(0.015)(6.4\text{m}) = 10.417 \text{ k}\Omega$ [1, 0.5]
 $(r_{o2} \parallel r_{o4} \parallel R_L) = (7.812\text{k}) \parallel (10.417\text{k}) \parallel (100\text{k}) = 4.273 \text{ k}\Omega$ [1.5]
 $A_d = (2.26 \text{ mA/V}) \times (4.273 \text{ k}) = 9.668$ [1]
- (c) $CMRR = 20 \log [A_d / A_{cm}] = 60 \text{ dB}$ [1, 0.5]
 $A_{cm} = A_d / [10^{(60/20)}] = 9.668 \times 10^{-3}$ [1, 0.5]
- (d) Select two answers: [2 marks]
 Increase the current source output resistance. [1]
 Increase the diff amp output resistance by using cascode active loads. [1]
 Reduce the loading effect by increasing load resistance, R_L . [1]

Answers for Question 2 (Continued)

(e)



Biassing [5], Diff Amp [5], Cascode Active Load [5]

Total [15 marks]

QUESTION 3 [20 marks]

Answers for Question 3

$$I_Q = 0.307mA$$

$$I_{C2} = I_{E2} = I_Q / 2 = 0.1531mA \quad [1]$$

$$I_{C7} = I_Q = 0.307mA \quad [1]$$

$$I_{C6} = I_{C7} / (1 + \beta) = 0.307m / 121 = 2.537\mu A \quad [1, 1]$$

$$I_{C8} = \frac{v_o - V^-}{R_4} = \frac{0 - V^-}{R_4} = 10 / 5k = 2mA \quad [1]$$

Calculation for R_i :

$$r_{\pi6} = \frac{\beta V_T}{I_{C6}} = \frac{(120)(0.026)}{2.537\mu} = 1.229M\Omega \quad [1]$$

$$r_{\pi7} = \frac{\beta V_T}{I_{C7}} = \frac{(120)(0.026)}{0.307m} = 10.16k\Omega \quad [1]$$

$$R_i = r_{\pi6} + (1 + \beta)r_{\pi7} = 1.229M + (121)(10.16k) = 2.459M\Omega \quad [1, 1]$$

Calculation for R_{c11} :

$$r_{\pi11} = \beta V_T / I_Q = (120 \times 0.026) / (0.307m) = 10.16k\Omega \quad [1]$$

$$R'_E = 10.16k \parallel 0.1k = 99\Omega \quad [1]$$

$$g_{m11} = I_Q / V_T = 0.307m / 0.026 = 11.81mA/V \quad [1]$$

$$r_{o11} = V_A / I_Q = 100 / 0.307m = 325.7k\Omega \quad [1]$$

Therefore, $R_{c11} = r_{o11} (1 + g_{m11} R'_E) = 706.5k\Omega \quad [1, 0.5]$

Calculation for R_{b8} :

$$r_{\pi8} = \beta V_T / I_{C8} = (100)(0.026) / (2m) = 1.3k\Omega \quad [1]$$

$$R_{b8} = r_{\pi8} + (1 + \beta)R_4 = 1.3k + (121)(5k) = 606.3k\Omega \quad [1, 0.5]$$

$$R_{L7} = R_{c11} \parallel R_{b8} = 706.5k \parallel 606.3k = 326.3k\Omega \quad [1]$$

$$r_{o7} = r_{o11} = V_A / I_Q = 100 / 0.307m = 325.7k\Omega \quad [1]$$

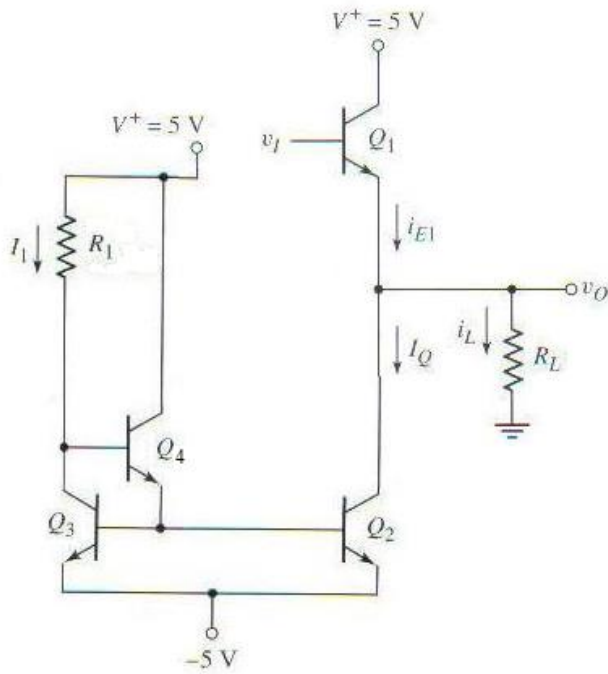
$$A_{v2} = [\beta(1 + \beta)(r_{o7} \parallel R_{L7})] / R_i$$

$$A_{v2} = [(120)(121)(326.3k \parallel 325.7k)] / 2.459M = 958.49 \quad [1]$$

QUESTION 4 [25 marks]

Answers for Question 4

4(a)



- 3-transistor current source [2]
- Emitter follower with V_I , V_O and R_L [2]
- Correct connection between EF & CS [1]

Answers for Question 4 (Continued)**Answers to QUESTION 4(b) [20 marks]**(i) Calculation of i_L , i_{Cp} , i_{Cn} , and v_I .**[12 marks]**

$$v_O = -4 \text{ V} = i_L R_L$$

$$\rightarrow i_L = v_O / R_L = (-4\text{V}) / (1\text{k}\Omega) = -4 \text{ mA} \quad [1]$$

Therefore, Q_p is conducting and Q_n is OFF.

$$\text{Approximate value } i_L \approx i_{Cp} = I_S \exp(V_{EBP} / V_T) = 4 \text{ mA} \quad [1]$$

$$V_{EBP} = V_T \ln(i_{Cp} / I_S) = (26\text{m}) \ln(4\text{m} / 2 \times 10^{-15}) = 0.7364 \text{ V} \quad [1]$$

$$V_{BEN} = V_{BB} - V_{EBP} = 1.4 - 0.7364 = 0.6636 \text{ V} \quad [1]$$

$$\rightarrow i_{Cn} = I_S \exp(V_{BEN} / V_T) = (2 \times 10^{-15}) \exp(0.6636 / 0.026) = 0.2429 \text{ mA} \quad [1]$$

$$i_{Cn} = i_{Cp} + i_L \quad [1]$$

$$\text{Actual value of } i_{Cp} = i_{Cn} - i_L = 0.2429\text{m} - (-4\text{m}) = 4.2429 \text{ mA} \quad [1]$$

$$V_{EBP} = V_T \ln(i_{Cp} / I_S) = (26\text{m}) \ln(4.2429 \text{ m} / 2 \times 10^{-15}) = 0.73796 \text{ V} \quad [1]$$

$$V_{BEN} = V_{BB} - V_{EBP} = 1.4 - 0.73796 = 0.66204 \text{ V} \quad [1]$$

$$\rightarrow i_{Cn} = I_S \exp(V_{BEN} / V_T) = (2 \times 10^{-15}) \exp(0.66204 / 0.026) = 0.2288 \text{ mA} \quad [1]$$

$$v_I = v_O + V_{BEN} - V_{BB} / 2 = -4 + 0.66204 - 0.7 = -4.03796 \text{ V}$$

$$\text{OR } v_I = v_O - V_{EBP} + V_{BB} / 2 = -4 - 0.73796 + 0.7 = -4.03796 \text{ V} \quad [2]$$

(ii) Calculation of power dissipation in transistor Q_n and Q_p :**[8 marks]**

$$P_{Qn} = i_{Cn} V_{CEn} \quad [2]$$

$$V_{CEn} = +V_{CC} - v_O = +6 - (-4) = 10 \text{ V} \quad [1]$$

$$\rightarrow P_{Qn} = (0.2288 \text{ mA})(10 \text{ V}) = 2.288 \text{ mW} \quad [1]$$

$$P_{Qp} = i_{Cp} V_{ECp} \quad [2]$$

$$V_{ECp} = v_O - (-V_{CC}) = -4 - (-6) = 2 \text{ V} \quad [1]$$

$$\rightarrow P_{Qp} = (4.2429 \text{ mA})(2 \text{ V}) = 8.4858 \text{ mW} \quad [1]$$