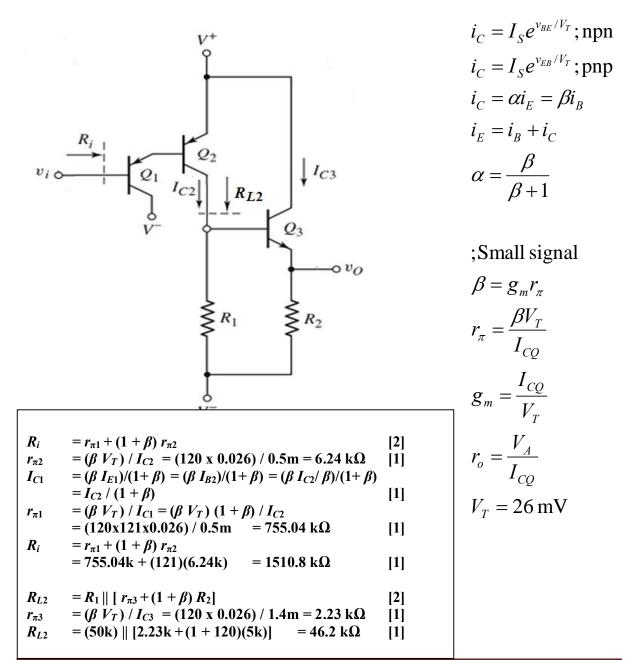
Name:Dr JBOStudent ID Number:Model AnswerSection:Lecturer:Dr. Jamaludin Bin Omar

## Question:

Study the gain stage and simple output stage circuit shown in Figure 1 carefully. The transistor parameters are:  $\beta = 120$  and  $V_A = 120$  V. Neglect base currents.

Let  $R_1 = 50 \text{ k}\Omega$ ,  $R_2 = 5 \text{ k}\Omega$ ,  $I_{C2} = 0.5 \text{ mA}$ , and  $I_{C3} = 1.4 \text{ mA}$ . Determine the input resistance ( $R_i$ ) of the gain stage and the equivalent load resistance of the gain stage connected to the collector of  $Q_2$ , i.e.  $R_{L2}$  shown in the Figure 1. [10 marks]

Write your answer using pen, with proper Units for all the parameters.



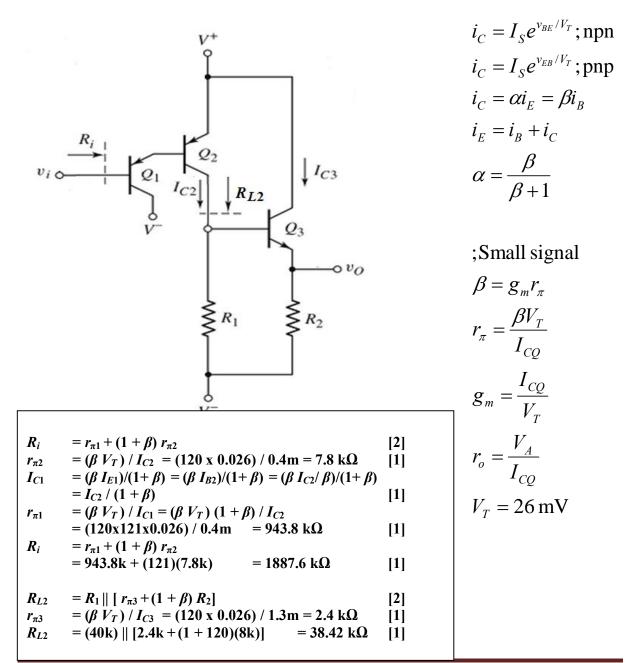
Name:Dr JBOStudent ID Number:Model AnswerSection:Lecturer:Dr. Jamaludin Bin Omar

## Question:

Study the gain stage and simple output stage circuit shown in Figure 1 carefully. The transistor parameters are:  $\beta = 120$  and  $V_A = 120$  V. Neglect base currents.

Let  $R_1 = 40 \text{ k}\Omega$ ,  $R_2 = 8 \text{ k}\Omega$ ,  $I_{C2} = 0.4 \text{ mA}$ , and  $I_{C3} = 1.3 \text{ mA}$ . Determine the input resistance ( $R_i$ ) of the gain stage and the equivalent load resistance of the gain stage connected to the collector of  $Q_2$ , i.e.  $R_{L2}$  shown in the Figure 1. [10 marks]

Write your answer using pen, with proper Units for all the parameters.



Prepared by: Dr Jamaludin Bin Omar

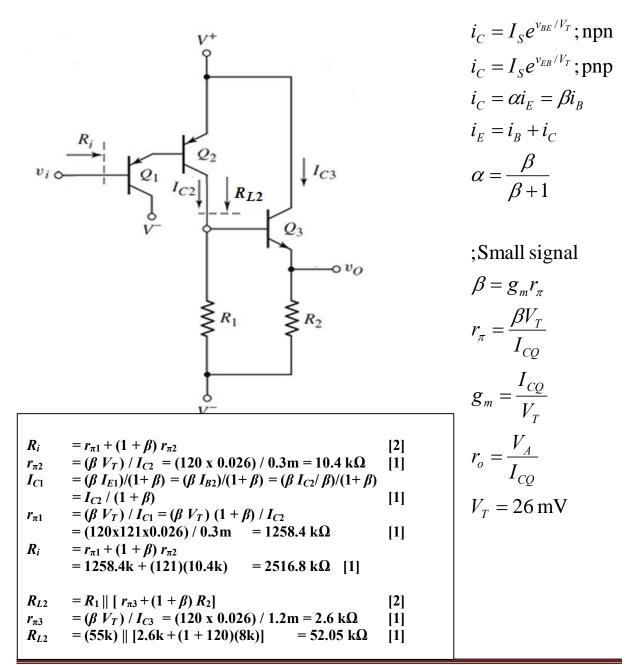
Name:Dr JBOStudent ID Number:Model AnswerSection:Image: Comparison of the sector of the sec

## Question:

Study the gain stage and simple output stage circuit shown in Figure 1 carefully. The transistor parameters are:  $\beta = 120$  and  $V_A = 120$  V. Neglect base currents.

Let  $R_1 = 55 \text{ k}\Omega$ ,  $R_2 = 8 \text{ k}\Omega$ ,  $I_{C2} = 0.3 \text{ mA}$ , and  $I_{C3} = 1.2 \text{ mA}$ . Determine the input resistance ( $R_i$ ) of the gain stage and the equivalent load resistance of the gain stage connected to the collector of  $Q_2$ , i.e.  $R_{L2}$  shown in the Figure 1. [10 marks]

Write your answer using pen, with proper Units for all the parameters.



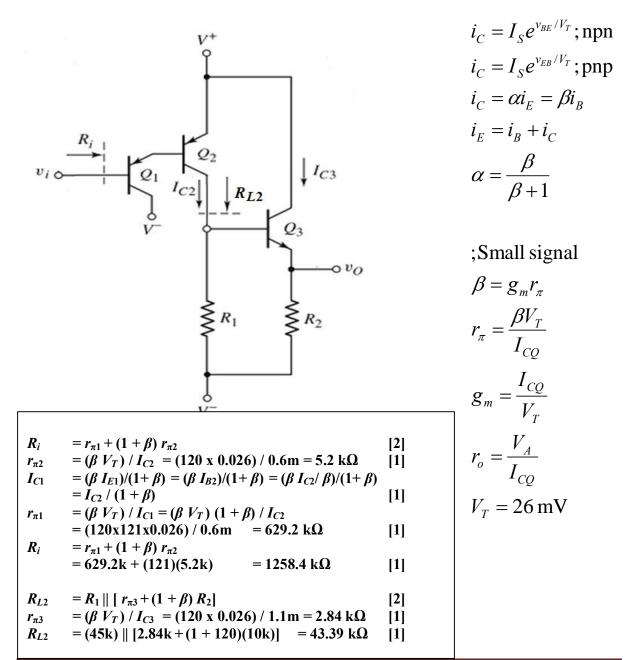
Name:Dr JBOStudent ID Number:Model AnswerSection:Image: Comparison of the sector of the sec

## Question:

Study the gain stage and simple output stage circuit shown in Figure 1 carefully. The transistor parameters are:  $\beta = 120$  and  $V_A = 120$  V. Neglect base currents.

Let  $R_1 = 45 \text{ k}\Omega$ ,  $R_2 = 10 \text{ k}\Omega$ ,  $I_{C2} = 0.6 \text{ mA}$ , and  $I_{C3} = 1.1 \text{ mA}$ . Determine the input resistance ( $R_i$ ) of the gain stage and the equivalent load resistance of the gain stage connected to the collector of  $Q_2$ , i.e.  $R_{L2}$  shown in the Figure 1. [10 marks]

Write your answer using pen, with proper Units for all the parameters.



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