

### **Decoder Exercise**

- 1) Draw the two 3-to-8 line decoders (uncomplemented outputs) using the AND and NOT gates that produce the 8 output(D0 – D7).
- 2) Draw the two 3-to-8 line decoders (uncomplemented outputs) to prove the truth tables above are true.

### **Flip Flop Exercise**

- 1) Draw the two SR Latch Circuit with 2 input(S & R)
- 2) Draw the two D Latch Circuit with 2 input(C & D)