

Digital Logic Lab Question

Question 1

- A) Using Cedar, draw the circuit diagram for the 2-bit by 2 bit Binary Multiplier.
- B) Provide the timing diagram for the circuit.

(Note: The adder is located at “6 – Add & Compare” in Cedar”).

Question 2

- A) Using Cedar, draw the Circuit diagram for a 4-bit by 3 bit multiplier.
- B) Solve this equation :-
 - i. 1011×111
 - ii. 1100×101
- C) Show the result using LED (C0, C1, C2, and C3).

Question 3

- A) Using Cedar, draw the circuit diagram for a 4 bit magnitude comparator.
- B) Save the .cld file for each comparator ($>$, $=$, $<$)
- C) Compare the value below.
 - i) 1010 and 0101
 - ii) 1100 and 1100
 - iii) 0100 and 1111