Signal Processing with MATLAB
A Hands-On Workshop

DATE: Friday April 15, 2016

VENUE: BL-3-010,
Control Systems Laboratory,
College of Engineering,
Universiti Tenaga Nasional,
Putrajaya Campus

FACILITATOR: Syed Khaleel Ahmed

ITINERARY

8:30am	Registration
9:00am	Introduction Representation of Signals Representation of Systems (Filters)
10:30am	Tea
10:45am	Representation of Systems (Filters) (contd.) Time-Domain Analysis
12:45pm	Lunch
2:15pm	Frequency Domain Analysis Filter Design
3:30pm	Tea
3:45pm	Graphical User Interface Signal Processing Blockset
5:30pm	Photo Session Certificates Presentation