



IEEE
Signal Processing Society
Malaysia Chapter

Signal Processing with MATLAB

A Hands-On Workshop

DATE: Friday April 14, 2017

VENUE: BL-3-010,
Control Systems Laboratory,
College of Engineering,
Universiti Tenaga Nasional,
Putrajaya Campus

FACILITATOR: Syed Khaleel Ahmed

ITINERARY

- 8:30am Registration & Breakfast
 - 9:00am Introduction
Representation of Signals
Representation of Systems (Filters)
 - 10:30am Tea
 - 10:45am Time-Domain Analysis
Frequency Domain Analysis
 - 12:45pm Lunch
 - 2:15pm Frequency Domain Analysis (contd.)
Filter Design
 - 3:30pm Tea
 - 3:45pm Graphical User Interface
DSP System Toolbox
 - 5:30pm Certificates Presentation
Photo Session
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